

Faculty Profile

Name	Bala Sindhuri Kandula
Date of Birth	04-07-1987
Unique ID (AICTE ID)	1-474327371
Education Qualifications	PhD
Work Experience	
a) Teaching	11
b) Research	6
c) Industry	-
d) Others	-
Area of Specialization	VLSI and Signal Processing
Courses taught at Under Graduate/ Post Graduate Level	Digital VLSI Design(UG) Computer Architecture and Organization(UG) VLSI Design(UG) VLSI Design(PG) Microprocessors and Applications(UG) Microprocessors and Microcontrollers(UG) Linear IC's and Applications(UG) Coding theory and Applications(PG) RM & IPR(PG) VLSI Laboratory (UG) Analog Electronics and Circuits(UG) Microprocessors and Applications Laboratory (UG) Microprocessors and Microcontrollers Laboratory (UG) Digital Logic Design Laboratory (UG) Digital Integrated Circuits Laboratory(UG) Switching Theory and Logic Design Laboratory (UG) Digital Signal Processing Laboratory (UG) Arduino Programming Laboratory (UG) Electronic Devices and Circuits Laboratory(UG) Analog Electronics and Circuits Laboratory(UG)
Research guidance	
a. No. of papers published in National/ International Journals/ Conferences	49
b. Master (No. of Completed/Ongoing)	7
c. Ph.D. (No. of Completed/Ongoing)	-
No. of Projects Carried out	-
No. of Patents (Filed & Granted)	1
Technology Transfer	-
Research Publications (No. of papers published in National/International Journals/Conferences)	61
No. of Books published	-